

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Zamir Ahmad and Jeffrey F. Wong

Title: REDUCED DELAY POWER FAIL-SAFE CIRCUIT

Application No.: 10/716,690

Filed: November 19, 2003

Examiner: Don P. Le

Group Art Unit: 2819

Atty. Docket No.: 004-9321

Conf. No.: 6566

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**REQUEST FOR RECONSIDERATION AND WITHDRAWAL OF
OBJECTION**

Dear Sir:

In the Examiner's Comment attached to the Notice of Allowability mailed May 10, 2005, the Office included an objection and requirement that one of Applicants' drawings be labeled as "Prior Art." This objection and requirement was first made at the time prosecution was closed. Applicants respectfully traverse the objection and request reconsideration and withdrawal of the objection for at least the reason that Figure 1 does not illustrate only that which is in the prior art and Applicants have not indicated otherwise.

Details and Interview Summary

In an effort to expediently resolve the issues, the undersigned contacted Examiner Le on June 29, 2005. Regarding the drawings, the Examiner stated that Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated and apparently relied on Figure 1 as depicting "an illustrative power fail-safe circuit, which is largely conventional in design," as recited in the specification. However, many drawings include aspects that are conventional in design. Applicants did not intend to convey, and Applicants do not admit, that Figure 1 is completely conventional in design. Accordingly, Applicants

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